

Chiplet Summit Announces its Initial Keynote Schedule

SAN DIEGO, CA – October 17, 2024 — Emphasis is on AI applications.

The third annual Chiplet Summit, at the Santa Clara Convention Center on January 21-23, 2025, has now set its initial keynote schedule.

Companies and organizations giving keynotes will be:

- Synopsys
- Teradyne
- Alphawave Semi
- Cadence
- Arm
- Keysight Technologies
- Open Compute Project

There will also be short talks by the UCle Consortium, Silicon Catalyst, and SNIA.

Keynote topics will include AI solutions, design methods, interfaces, new packaging methods, and the open chiplet economy. Attendees will learn how chiplets can make the latest designs take less time, cost less, and be more scalable.

Summit General Chair Chuck Sobey says, “AI is an obvious driver. It requires huge amounts of memory, fast low-latency connections, and the ability to do both training and model execution at top speed. At our keynotes, attendees will hear top industry leaders’ plans to overcome these challenges.”

To discuss exhibiting, contact:

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About Chiplet Summit

Chiplet Summit, a product of Semper Technologies, showcases the emerging chiplet market. It features the people who are using chiplets in designs for processors, communications chips, and AI devices.

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